

INVENTOR INFORMATION

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APPLICATION INFORMATION

Title Line One:: METHOD OF MANUFACTURING SEMICONDUCTOR DE
Title Line Two:: VICE AND SEMICONDUCTOR DEVICE
Total Drawing Sheets:: 24
Formal Drawings?: Yes
Application Type:: Utility
Docket Number:: 740756-2410
Secrecy Order in Parent Appl.?: No

REPRESENTATIVE INFORMATION

Registration Number One:: 35483
Registration Number Two:: 38285
Registration Number Three:: 45285
Registration Number Four:: 27997
Registration Number Five:: 32815

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2000-384543
Filing Date:: 12-19-2000
Country:: Japan
Priority Claimed:: Yes
Foreign Application Two:: 2000-403098
Filing Date:: 12-28-2000
Country:: Japan
Priority Claimed:: Yes

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